

ABSTRACT OF THE DISCLOSURE

An input signal V_{in} is converted into a digital signal by an AD converter 10 and output as a digitized input signal
5 V_{ind}. A counter circuit 20 sets a count number based on the digitized input signal V_{ind} and starts to count a counter clock signal CLK_c upon a trigger signal TRG, and generates an output signal S_{out} when the count number is obtained.